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**Deliver To:** Examiner, Nguyen, Joseph T.  
**Company:** United States Patent and Trademark Office  
**Fax Number:** (571) 273-8300  
**From:** Richard P. Berg (Registration No. 28,145)  
**Date:** March 16, 2006  
**Total No. of Pages (Including This Page):** 18  
**Subject:** Response  
U.S. Patent Application No. 09/768,904  
Applicant: Lap-Wei Chow, et al.  
"Integrated Circuit Protection,..."  
Our Ref: B-3964/618029-8/RPB

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Dear Examiner Nguyen:

Enclosed is a Response to the final Office Action mailed on December 16, 2005, consisting of:

Response - 14 pages  
Fundamentals of Semiconductor, et al. Bu Badih El-Karch

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Lap-Wai Chow, et al.	)	Examiner: Nguyen, Joseph H.
		)	
Serial No.	09/768,904	)	Art Unit: 2815
		)	
Filed:	January 24, 2001	)	Re: Response to Final Office Action
		)	
For:	"Integrated Circuit Protection..."	)	Our Ref: B-3964 618029-8
		)	
		)	Date: March 16, 2006

RESPONSE TO FINAL OFFICE ACTION

Box AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Sir:

This paper is filed in response to the final Office Action dated December 16, 2005.

Remarks begin on Page 2 of this response.